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[James J. Leifer](#), [Gilles Peskine](#), [Peter Sewell](#), [Keith Wansbrough](#)

 August 2003 **ICFP '03**: Proceedings of the eighth ACM SIGPLAN international conference on Functional programming
**Publisher:** ACM
 Full text available: Pdf (226.25 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)
**Bibliometrics:** Downloads (6 Weeks): 1, Downloads (12 Months): 14, Citation Count: 9

Type abstraction is a key feature of ML-like languages for writing large programs. Marshalling is necessary for writing distributed programs, exchanging values via network byte-streams or persistent stores. In this paper we combine the two, developing ...

**Keywords:** ML, abstract types, distributed programming, hashing, lambda calculus, marshalling, modules, programming languages, serialisation, singleton kinds, type theory

Also published in:

September 2003 **SIGPLAN Notices** Volume 38 Issue 9

### 2 [Security as a new dimension in embedded system design](#)

[Paul Kocher](#), [Ruby Lee](#), [Gary McGraw](#), [Anand Raghunathan](#) / [Srivaths Ravi](#)

 June 2004 **DAC '04**: Proceedings of the 41st annual conference on Design automation
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The growing number of instances of breaches in information security in the last few years has created a compelling case for efforts towards secure electronic systems. Embedded systems, which will be ubiquitously used to capture, store, manipulate, and ...

**Keywords:** PDAs, architectures, battery life, cryptography, design, design methodologies, digital rights management, embedded systems, performance, security, security processing, security protocols, sensors, software attacks, tamper resistance, trusted computing, viruses

3 [Technical opinion: designing cryptography for the new century](#)



[Susan Landau](#)

May 2000 **Communications of the ACM** , Volume 43 Issue 5

**Publisher:** ACM

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4 [Securing Mobile Appliances: New Challenges for the System Designer](#)

[Anand Raghunathan](#), [Srivaths Ravi](#), [Sunil Hattangady](#), [Jean-Jacques Quisquater](#)

March 2003 **DATE '03: Proceedings of the conference on Design, Automation and Test in Europe - Volume 1** , Volume 1

**Publisher:** IEEE Computer Society

Full text available: [Publisher Site](#) , [Pdf](#) (257.28 KB)

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As intelligent electronic systems pervade all aspects of our lives, capturing, storing, and communicating a wide range of sensitive and personal data, security is emerging as a critical concern that must be addressed in order to enable several current ...

5 [Performance Comparison of Security Mechanisms for Grid Services](#)

[Satoshi Shirasuna](#), [Aleksander Slominski](#), [Liang Fang](#), [Dennis Gannon](#)

November 2004 **GRI D '04: Proceedings of the 5th IEEE/ACM International Workshop on Grid Computing**

**Publisher:** IEEE Computer Society

Full text available: [Pdf](#) (145.66 KB)

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Security is one of the most important features for Grid services. There are several specifications used to add security to Grid services, and some of them have been implemented and are in use. However, since most of the security mechanisms involve slow ...

6 [Security in embedded systems: Design challenges](#)



[Srivaths Ravi](#), [Anand Raghunathan](#), [Paul Kocher](#), [Sunil Hattangady](#)

August 2004 **Transactions on Embedded Computing Systems (TECS)** , Volume 3 Issue 3

**Publisher:** ACM

Full text available: Pdf (3.67 MB)

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Many modern electronic systems---including personal computers, PDAs, cell phones, network routers, smart cards, and networked sensors to name a few---need to access, store, manipulate, or communicate sensitive information, making security a serious concern ...

**Keywords:** Embedded systems, architecture, authentication, battery life, cryptographic algorithms, decryption, encryption, hardware design, processing requirements, security, security attacks, security protocols, tamper resistance

7 [Energy characterization of a tiled architecture processor with on-chip networks](#)



[Jason Sungtae Kim](#), [Michael Bedford Taylor](#), [Jason Miller](#), [David Wentzlaff](#)

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Tiled architectures provide a paradigm for designers to turn silicon resources into processors with burgeoning quantities of programmable functional units and memories. The architecture has a dual responsibility: first, it must expose these resources ...

**Keywords:** power, raw microprocessor, scalar operand network, tile

8 [Architectural Support for High Speed Protection of Memory Integrity and Confidentiality in Multiprocessor Systems](#)

[Weidong Shi](#), [Hsien-Hsin S. Lee](#), [Mrinmoy Ghosh](#), [Chenghual Lu](#)

September 2004 **PACT '04: Proceedings of the 13th International Conference on Parallel Architectures and Compilation Techniques**

**Publisher:** IEEE Computer Society

Full text available: Pdf (255.33 KB)

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Recently there is a growing effort in both the architecture and the security community to create a hardware solution for authenticating system memory. As shown in the previous work, hardware-based memory authentication will become a vital component for ...

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